

respectively. A moving image signal output circuit 43 and a still image signal output circuit 41 are connected to the signal line 45. A moving image vertical direction selecting circuit 52 and a still image vertical direction selecting circuit 51 are connected to the vertical direction gate selection line 50. A moving image horizontal direction selecting circuit 44 and a still image horizontal direction selecting circuit 42 are connected to the horizontal direction gate selection line 46. The moving image signal output circuit 43, the still image signal output circuit 41, the moving image vertical direction selecting circuit 52, the still image vertical direction selecting circuit 51, the moving image horizontal direction selecting circuit 44, and the still image horizontal direction selecting circuit 42 are connected to the write signal generating circuit 17.--

Delete the paragraph on page 19, lines 11-16, and replace it with the following replacement paragraph:

--According to the embodiments, the image display having high picture quality of hundreds of dots/inch which is about the same as that of a printed matter and (thousands × thousands) display pixels can be realized without hardly increasing the rewriting speed of the display pixels.--

REMARKS

The title and the specification have been amended.

Claims 1-15 are pending, with claims 1, 10, and 14-15 being independent.

Attached hereto is an Appendix entitled "Version with Markings to Show Changes Made" which is a marked-up version of the portions of the application which have been amended by the present preliminary amendment, with brackets indicating deleted matter, underlining indicating added matter, and double underlining indicating added material that is to be underlined in the printed patent.

Submitted herewith is an Information Disclosure Statement, consideration of which is respectfully requested.

Claims 1-15 of the present application are respectively identical to allowed claims 1, 3, 14, 4, 6-7, 11-13, 2, 8-10, and 16-17 of application Serial No. 09/043,534, the parent application of the present continuation application, except that the term AND logical circuit in the last paragraph of allowed independent claims 1-2 and 16-17 of the parent application has been changed to AND functional circuit in claims 1, 10, and 14-15 of the present application which respectively correspond to allowed claims 1-2 and 16-17 of the parent application. It is submitted that claims 1-15 of the present application are allowable over the prior art for substantially the same reasons that allowed claims 1, 3, 14, 4, 6-7, 11-13, 2, 8-10, and 16-17 of the parent application to

which claims 1-15 of the present application respectively correspond are allowable over the prior art.

Please charge any shortage in fees due in connection with the filing of this paper, or credit any overpayment of fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (520.36114CX1).

Respectfully submitted,

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